

# Call for Participation

## Hardware design and Functional Languages

**Braga, Portugal, 24–25 March 2007**

A satellite event of the **ETAPS 2007** group of conferences

More abstract representations and verification techniques are needed to keep up with the ever-increasing complexity of modern hardware designs. We face challenging research problems, many of them related to language design and to ways of modelling circuits at various levels of abstraction. To achieve the necessary breakthroughs, we must ensure that academics and industrial researchers continue to work together to solve the real challenge of hardware design and verification. This workshop will bring together researchers in modern functional languages, hardware description languages, high-level modelling and validation, and formal design environments. It aims to present the state of the art, and to spark debate about how to proceed.

The organizers of HFL 2007 are pleased to announce an exciting 2-day programme, consisting of fourteen talks from the industry and academia. For more information, including an advance programme, please visit the workshop web-page at <http://www.hflworkshop.org/>. To register, please visit the ETAPS 2007 web-page at <http://www.di.uminho.pt/etaps07>.

### HFL 2007 Workshop Committee

Arvind (MIT)  
Jim Grundy (Intel Corporation)  
Warren A. Hunt, Jr. (University of Texas at Austin)  
Steven D. Johnson (Indiana University)  
Andy Martin (IBM Research)  
Tom Melham (University of Oxford)  
Simon Peyton Jones (Microsoft Research)  
Marc Pouzet (University of Paris-Sud)  
Brian Rogoff (ARM Physical IP, Inc.)  
Carl Johan Seger (Intel Corporation)  
Tim Sheard (Portland State University)  
Mary Sheeran (Chalmers)  
Satnam Singh (Microsoft Corporation)  
Joe Stoy (Bluespec, Inc.)  
Walid Taha (Rice University)  
Matthew Wilding (Rockwell Collins Advanced Technology Center)

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